



■ LVDS SMD VCXO 7.0×5.0mm

- Differential output LVDS
- Available 3-State function
- Low jitter
- Mode : Fundamental, Multi

■ Applications : Radio transmitter - receiver equipment. Optical transmission device, Digital image devices.

■ Standard Specification

Frequency Range	1.000MHZ to 80.000MHZ(Fund)	
Supply voltage	2.5V, 3.3V	3.3V
Pin 1 Control voltage	1.25V±1.05V(1.25V)	1.65V±1.35V(1.65V)
Frequency Deviation	$\pm 100\text{ppm}$ min	
Linearity	10% max	
Slope	Positive	
Current consumption	80mA max	
Temperature Range	Operating	-40°C to +85°C, User spec
	Storage	-55°C to +125°C
Frequency Stability	$\pm 50\text{ppm}$ max, User spec	
Output load condition	100Ω OUT - OUTN	
Symmetry (Duty)	45% to 55%	
Output rise and fall time (tr/tf)	1ns max (20% ~ 80% Output Level)	
OE High / OE Low	0.7Vdd min / 0.3Vdd max	
High output voltage	1.4V min ~ 1.6V max	
Low output voltage	0.9V min ~ 1.1V max	
Pin 2 Input voltage	No Connection (Default)	
Pin 2 Input voltage	70% Vdd min (Output enable)	
Pin 2 Input voltage	30% Vdd max (Output disable)	
Output Differential Voltage	0.247V ~ 0.454V	
Offset Voltage	1.125V ~ 1.375V	
Oscillation start up time	5ms max	
Aging	$\pm 3\text{ppm}$ max	
phase Jitter (12KHZ ~20MHz)	1ps max	4ps max

■ Dimensions (mm)

